

Attorney's Docket No.5646-91XX

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Kee Park et al.

Serial No.: To Be Assigned

Filed: Concurrently Herewith

For: CONTENT ADDRESSABLE MEMORY (CAM) DEVICES WITH DUAL-FUNCTION CHECK BIT CELLS THAT SUPPORT COLUMN REDUNDANCY AND CHECK BIT CELLS WITH REDUCED SUSCEPTIBILITY TO SOFT ERRORS

Date: July 15, 2003

Mail Stop Patent Application

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a list of documents on form PTO-1449 together with a copy of each identified document. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.97 and Section 609 of the MPEP.

No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to our Deposit Account No. 50-0220.

Respectfully submitted,

  
Grant J. Scott  
Registration No. 36,925



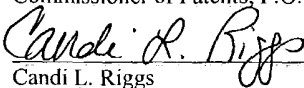
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PATENT TRADEMARK OFFICE

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Date of Deposit: July 15, 2003

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Mail Stop Patent Application, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

  
Candi L. Riggs

<b>FORM PTO-1449</b> U.S. Department of Commerce Patent and Trademark Office  <b>LIST OF DOCUMENTS CITED BY APPLICANT</b>  (Use several sheets if necessary)				Attorney Docket Number 5646-91XX		Serial No. To Be Assigned	
				Applicants: Kee Park et al.			
				Filing Date: Concurrently Herewith		Group	
<b>U. S. PATENTS &amp; PATENT APPLICATION PUBLICATIONS</b>							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
	1	6,563,754	5-13-03	Lien et al.	365	222	
	2	6,560,156	5-6-03	Lien et al.	365	222	
	3	6,542,391	4-1-03	Pereira et al.	365	49	
	4	6,538,911	3-25-03	Allan et al.	365	49	
	5	6,434,033	08/13/02	Chien			
	6	6,430,073	8-6-02	Batson et al.	365	49	
	7	6,408,417	06/18/02	Moudgal et al.			
	8	6,397,290	05/28/02	Williams et al.			
	9	6,381,191	04/30/02	Ooishi			
	10	6,374,325	04/16/02	Simpson et al.			
	11	6,324,087	11-27-01	Pereira	365	49	
	12	6,289,471	09/11/01	Gordon			
	13	6,233,717	05/15/01	Choi			
	14	6,219,815	04/17/01	DesJardins et al.			
	15	6,216,246	04/10/01	Shau			
	16	6,188,629	02/13/01	Kaplinsky			
	17	6,151,247	11/21/00	Estakhri et al.			
	18	6,134,631	10/17/00	Jennings, III			
	19	6,067,656	5-23-00	Rusu et al.	714	768	
	20	6,058,500	05/02/00	DesJardins et al.			
	21	6,032,214	02/29/00	Farmwald et al.			
	22	6,009,548	12/28/99	Chen et al.			
	23	5,761,222	06/02/98	Baldi			
	24	5,727,003	03/10/98	Zook			
	25	5,699,369	12/16/97	Guha			
	26	5,682,394	10/28/97	Blake et al.			

EXAMINER \_\_\_\_\_

DATE CONSIDERED \_\_\_\_\_

In accordance with MPEP 609; draw line through citation if not in

<b>FORM PTO-1449</b> U.S. Department of Commerce Patent and Trademark Office  LIST OF DOCUMENTS CITED BY APPLICANT  (Use several sheets if necessary)				Attorney Docket Number 5646-91XX		Serial No. To Be Assigned	
				Applicants: Kee Park et al.			
				Filing Date: Concurrently Herewith		Group	
	27	5,644,583	07/01/97	Garcia et al.			
	28	5,629,950	05/13/97	Godiwala et al.			
	29	5,604,753	02/18/97	Bauer et al.			
	30	5,570,377	10/29/96	Merino Gonzalez et al.			
	31	5,469,450	11/21/95	Cho et al.			
	32	5,450,424	09/12/95	Okugaki et al.			
	33	5,233,614	08/03/93	Singh			
	34	5,127,014	06/30/92	Raynham			
	35	4,958,352	09/18/90	Noguchi et al.			
	36	4,903,268	02/20/90	Hidaka et al.			
	37	2003/0081464	5-1-03	Vlasenko	365	200	
	38	2003/0074630	4-17-03	Batson et al.	714	805	
	39	2003/0007408	01/09/003	Lien et al.	365	222	
	40	2002/0159320	10/31/02	Lien et al.			
	41	2002/0067632	06/06/02	Batson et al.			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
	42	Halsall, Fred, "Data Communications, Computer Networks and OSI," Second Edition, 11 pages, No Date					
	43	Fourouzan, Behrouz, "Introduction to Telecommunications and Networking," Chapter 8, pp. 194-226, No Date					
	44	Kalter et al., "A 50-ns 16-Mb DRAM with a 10-ns Data Rate and On-Chip ECC," IEEE Journal of Solid-State Circuits, Vol. 25, No. 5, October 1990, pp. 1118-1127					